

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 251119US2SRD		SERIAL NO. 10/808,280	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Tatsunori KANAI, et al.			
				FILING DATE March 25, 2004		GROUP	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
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	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
/K.W.L./	AO	10-143380	05/29/1998	JAPAN			X
/K.W.L./	AP	8-180025	07/12/1996	JAPAN			X
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
	AW						
	AX						
	AY						
	AZ						<input type="checkbox"/> Additional References sheet(s) attached
Examiner				/Kwok W. Lee/ (10/12/2007)		Date Considered	
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

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Tatsunori KANAI, et al.

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					YES	NO
/K.W.L./	AO	10-232856	9/2/1998	Japan		X
/K.W.L./	AP	11-73216	3/16/1999	Japan		X
/K.W.L./	AQ	3-205985	9/9/1991	Japan		X
/K.W.L./	AR	8-63442	3/8/1996	Japan		X
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

	AW	
	AX	
	AY	
	AZ	

☐ Additional References sheet(s) attached

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SHEET 1 OF 1

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/K.W.L.	AW	T. Carpenter, et al., "ARINC 659 Scheduling: Problem Definition", IEEE, Proceedings, Real-Time Systems Symposium, XP-010100430, December 7, 1994, pages 165-169				
/K.W.L.	AX	Jia XU, "Multiprocessor Scheduling of Processes with Release Times, Deadlines, Precedence, and Exclusion Relations", IEEE Transactions on Software Engineering, XP-000381700, vol. 19, no. 2, February 1, 1993, pages 139-154				
/K.W.L.	AY	Terry SHEPARD, et al., "A Pre-Run-Time Scheduling Algorithm For Hard Real-Time Systems", IEEE Transactions on Software Engineering, XP-000261408, vol. 17, no. 7, July 1, 1991, pages 669-677				
	AZ				<input type="checkbox"/> Additional References sheet(s) attached	

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LIST OF RELATED CASES

	<u>Docket Number</u>	<u>Serial or Patent Number</u>	<u>Filing or Issue Date</u>	<u>Patent Appl. Publication No.</u>	<u>Inventor/ Applicant</u>
/K.W.L./	251119US2SRD*	10/808,280	03/25/04		KANAI et al.
/K.W.L./	256214US2SRD	10/895,040	07/21/04		KANAI et al.

/Kwok W. Lee/ (10/25/2007)



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/K.W.L./	251119US2SRD*	10/808,280	03/25/04		KANAI et al.
/K.W.L./	256733US2SRD	10/902,938	08/02/04		KANAI et al.

/Kwok W. Lee/ (10/25/2007)

*Present Application; listed for information

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/K.W.L./	251119US2SRD*	10/808,280	03/25/04		KANAI et al.
/K.W.L./	258598US2SRD	10/935,188	09/08/04		KANAI et al.

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/K.W.L./	278844US2SRD	11/232,984	09/23/05		MAEDA, et al.
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/Kwok W. Lee/ (10/25/2007)

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